ABSTRACT OF THE DISCLOSURE

A solid-state image sensor device having an image sensing portion performing the photoelectric conversion and being able to correspond to both progressive mode in which all picture element signals obtained by the scanning of one time are output independently, and interlaced mode in which a plurality of interlaced scannings are performed and the picture element signals obtained by respective interlaced scannings are superposed, comprises a substrate-bias generation circuit for applying a bias voltage to the substrate of the image sensing portion, and for controlling the value of the bias voltage in the progressive mode to be smaller in comparison with that in the interlaced mode.